



Attorney Docket No.: 5693P286

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application for:

Vijayan Rajan et al.

Serial No.: 09/828,271

Filing Date: 4/5/2001

For: SYMMETRIC
MULTIPROCESSOR
SYNCHRONIZATION USING
MIGRATING SCHEDULING
DOMAINS

Examiner: Vo, Lilian

Group Art Unit: 2195

Mail Stop RCE
Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT AND NOTICE OF OTHER PENDING APPLICATION(S)

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Applicants note that Office Actions have been received in related, copending applications. In particular, an Office Action for U.S. Patent Application No. 09/828,284 was mailed on 11/02/05 and 05/12/05. Copies of these Office Actions are available through the Patent Application Information Retrieval (PAIR) system. If the Examiner so requests, Applicants will provide the Examiner with a copy of these Office Actions.

Applicant would like to bring to the Examiner's attention the following co-pending patent application(s), which is/are assigned to the assignee of the present application, and which may be relevant to the present application:

- 1) U.S. patent application no. 10/829,718, entitled, "Method and Apparatus for Allocating Resources in a Shared Resource Processor," filed on April 21, 2004, of Alexander D. Petruncola et al.

Applicants respectfully request that the above-listed items not be printed on the face of any patent which may issue from the present application.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability or constitutes prior art with respect to the present invention.


Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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- 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- A statement pursuant to 37 C.F.R. §1.97(e) or
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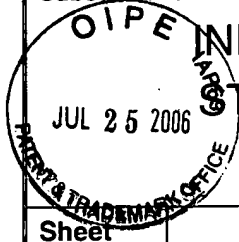
Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7/21/06


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Substitute for Form 1449/PTO



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/828,271
Filing Date	04/05/2001
First Named Inventor:	Vijayan Rajan et al.
Art Unit	2195
Examiner Name	Vo, Lilian
Attorney Docket Number	5693P286

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)				
		US-	5,644,709	7-1-1997	Austin	
		US-	5,872,909	2-16-1999	Wilner et al.	
		US-	6,151,618	11-21-2000	Wahbe et al.	
		US-	2001/0037450	11-1-2001	Metlitski et al.	
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FOREIGN PATENT DOCUMENTS

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS.

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<div style="border: 1px solid black; border-radius: 50%; padding: 10px; display: inline-block; transform: rotate(-15deg);"> 61 Pgs JUL 25 2006 PATENT & TRADEMARK OFFICE </div>				Application Number: 09/828,271	
				Filing Date: 04/05/2001	
				First Named Inventor: Vijayan Rajan et al.	
				Art Unit: 2195	
				Examiner Name: Vo, Lilian	
Sheet 2 of 2				Attorney Docket Number: 5693P286	
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²		
		"IA-32 Intel Architecture Software Developer's Manual. Volume 3: System Programming Guide," 2004, 26 pgs, Intel Corporation			

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